

## STPS40L45C-Y

## Automotive power Schottky rectifier

### Datasheet – production data

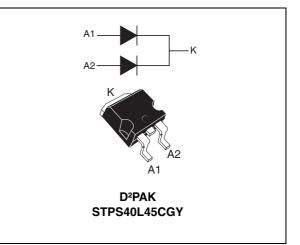
### Features

- Low forward voltage drop meaning very small conduction losses
- Low switching losses allowing high frequency operation
- Avalanche capability specified
- AEC-Q101 qualified

## Description

Dual center tap Schottky barrier rectifier designed for high frequency switched mode power supplies and DC to DC converters.

Packaged in D<sup>2</sup>PAK, this device is intended for use in low voltage, high frequency inverters, freewheeling and polarity protection for automotive applications.



### Table 1.Device summary

Symbol	Value
I <sub>F(AV)</sub>	2 x 20 A
V <sub>RRM</sub>	45 V
T <sub>j</sub> (max)	150 °C
V <sub>F</sub> (max)	0.49 V

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This is information on a product in full production.

## 1 Characteristics

Symbol	Parameter			Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage			45	V
I <sub>F(RMS)</sub>	Forward rms current			30	А
I <sub>F(AV)</sub>	Average forward current	$\begin{array}{c c} T_c = 130 \ ^\circ C \\ \delta = 0.5 \end{array}  \begin{array}{c} \text{per diode} \\ \text{per device} \end{array}$		20 40	A
I <sub>FSM</sub>	Surge non repetitive forward current	t <sub>p</sub> = 10 ms sir	nusoidal	230	А
I <sub>RRM</sub>	Repetitive peak reverse current	t <sub>p</sub> = 2 μs squa	are F = 1 kHz	2	А
I <sub>RSM</sub>	Non repetitive peak reverse current	3	А		
P <sub>ARM</sub>	Repetitive peak avalanche power	8100	W		
T <sub>stg</sub>	Storage temperature range			-65 to + 150	°C
Тj	Operating junction temperature <sup>(1)</sup>			-40 to + 150	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/µs
dPtot _ 1 _ condition to avoid thermal rungway for a diade on its own heateink					

### Table 2. Absolute ratings (limiting values, per diode)

1.  $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

### Table 3.Thermal resistances

Symbol	Parameter	Value	Unit	
R <sub>th (j-c)</sub>	Junction to case		1.5 0.8	°C/W
R <sub>th(c)</sub>	Coupling	0.1	°C/W	

When the diodes 1 and 2 are used simultaneously :

 $\Delta T_{i}$ (diode 1) = P(diode1) x R<sub>th(i-c)</sub>(Per diode) + P(diode 2) x R<sub>th(c)</sub>.

 Table 4.
 Static electrical characteristics (per diode)

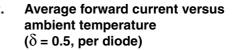
Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
IR <sup>(1)</sup> Reverse leakage current	Reverse leakage	T <sub>j</sub> = 25 °C	V _ V			0.6	mA
	T <sub>j</sub> = 125 °C	$V_{R} = V_{RRM}$		140	280	mA	
V <sub>F</sub> <sup>(1)</sup> Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 20 A			0.53		
	Forward voltage drop	T <sub>j</sub> = 125 °C	I <sub>F</sub> = 20 A		0.42	0.49	V
	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 40 A			0.69	v
		T <sub>j</sub> = 125 °C	I <sub>F</sub> = 40 A		0.6	0.7	

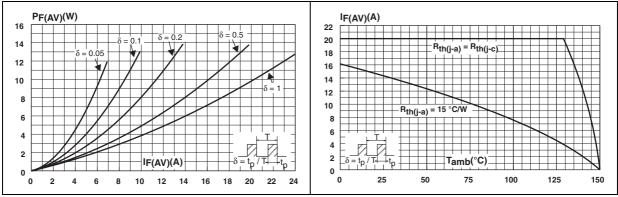
1. Pulse test:  $t_p = 380 \ \mu s, \ \delta < 2\%$ 

To evaluate the conduction losses use the following equation: P = 0.28 x  $I_{F(AV)}$  + 0.0105  ${I_F}^2_{(RMS)}$ 



### Figure 1. Average forward power dissipation Figure 2. versus average forward current (per diode)





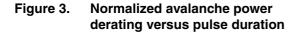


Figure 4. Normalized avalanche power derating versus junction temperature

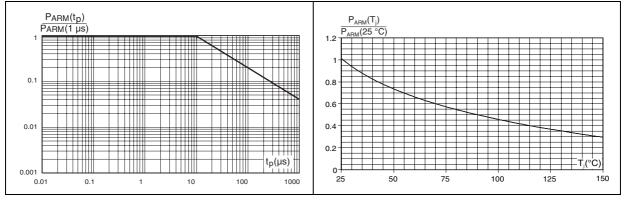
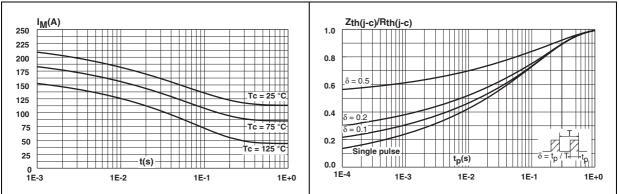


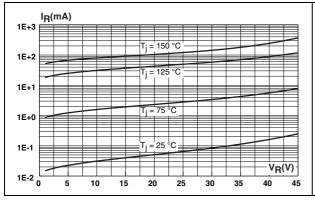
Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to case versus pulse duration

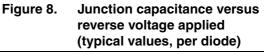




# Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)



### Figure 9. Forward voltage drop versus forward current (maximum values, per diode)



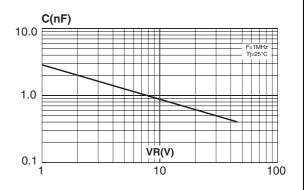
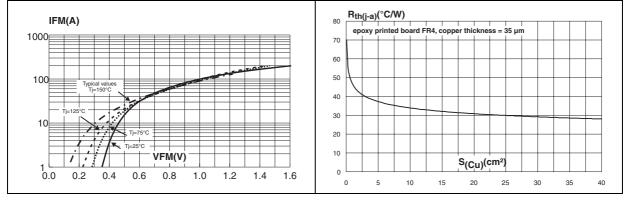


Figure 10. Thermal resistance junction to ambient versus copper surface under tab.



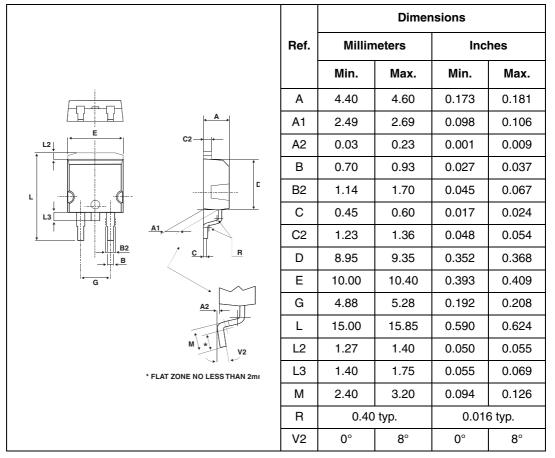


## 2 Package information

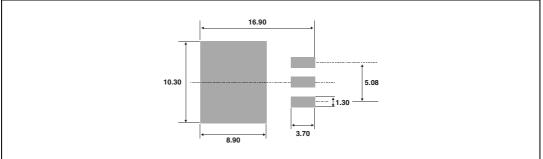
- Epoxy meets UL94, V0
- Cooling method: by conduction (C)

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.

Table 5. D<sup>2</sup>PAK dimensions



### Figure 11. Footprint (dimensions in mm)





## **3** Ordering information

### Table 6.Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS40L45CGY-TR	STPS40L45CGY	D <sup>2</sup> PAK	1.8 g	500	Tape and Reel

## 4 Revision history

### Table 7.Document revision history

Date	Revision	Changes
25-Jun-2012	1	First issue.



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